

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

# U.S. UTILITY Patent Application

# O.I.P.E.

**PATENT DATE**

SCANNED

**Q.A**

Bw

APPLICATION NO. 09/745988	CONT/PRIOR F	CLASS 714	SUBCLASS 785	ART UNIT 2133	EXAMINER Moise DeCady
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**TITLE** **APPLICANTS**

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Built-in self test system and method for high speed clock and data recovery circuit

PTO-2040  
12/99

# ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		<b>ISSUE FEE</b> Amount Due: _____ Date Paid: _____	
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